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7590

05/07/2004

VOLENTINE FRANCOS, PLLC
 12200 Sunrise Valley Drive Suite 150
 Reston, VA 20191

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(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/092,499	03/08/2002	Noriyuki Miura	OKI.313	1269

TITLE OF INVENTION: METHOD OF MANUFACTURING A SILICON-ON-INSULATOR (SOI) SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	08/09/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LE, DUNG ANH	2818	438-207000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☒ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Oki Electric Industry Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) Adam E. Volentine

(Date)

8/9/2004

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01 FC:1501

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TRANSMIT THIS FORM WITH FEE(S)

TRANSMITTAL OF PAYMENT OF ISSUE FEE (Large Entity) (37 C.F.R. 1.311)					Docket No. OKI.313	
Applicant(s): Noriyuki Mura						
Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.	
10/092,499	03/08/2002	Le, Dung Anh	20987	2818	1269	
Invention: METHOD OF MANUFACTURING A SILICON-ON-INSULATOR (SOI) SEMICONDUCTOR DEVICE						
Mail Stop Issue Fee COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450						
Transmitted herewith are the following for the above-identified application.						
<input checked="" type="checkbox"/> Issue Fee Transmittal Form PTOL-85						
<input checked="" type="checkbox"/> Utility Fee: <u> \$ 1330.00 </u> <input type="checkbox"/> Design Fee: <u> </u> <input type="checkbox"/> Plant Fee: <u> </u>						
<input checked="" type="checkbox"/> Publication Fee: <u> \$ 300.00 </u>						
<input checked="" type="checkbox"/> A check in the amount of <u> \$1,645.00 </u> is attached.						
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <u> 50-0238 </u> as described below.						
<input type="checkbox"/> Charge the amount of						
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ADAM C. VOLENTINE REG. NO. 33289						
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